

Model Name: GA-H61M-S2PT

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*1 SLOT
16	IT8892E
17	PCI SLOT1&2
18	ITE 8620 LPC IO
19	COM,KB_USB,USB_ESATA,-PROCHOT
20	HWM,FAN CTRL,OV,
21	DUAL BIOS
22	FP,FUSB,SPK,SATALED
23	Realtek ALC887-VD2
24	REAR AUDIO JACK
25	REALTEK RTL8111F-VL
26	DISCRETE POWER
27	ATX

SHEET

TITLE

28	LINEAR CPU_VTT
29	VCORE ISL95836_1
30	VCORE ISL95836_2
31	VCORE ISL95836_3
32	LPT

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Title			Cover Sheet
Size	Document Number	GA-H61M-S2PT	
Custom		Rev	1.0
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Revision 1.0

### Component value change history

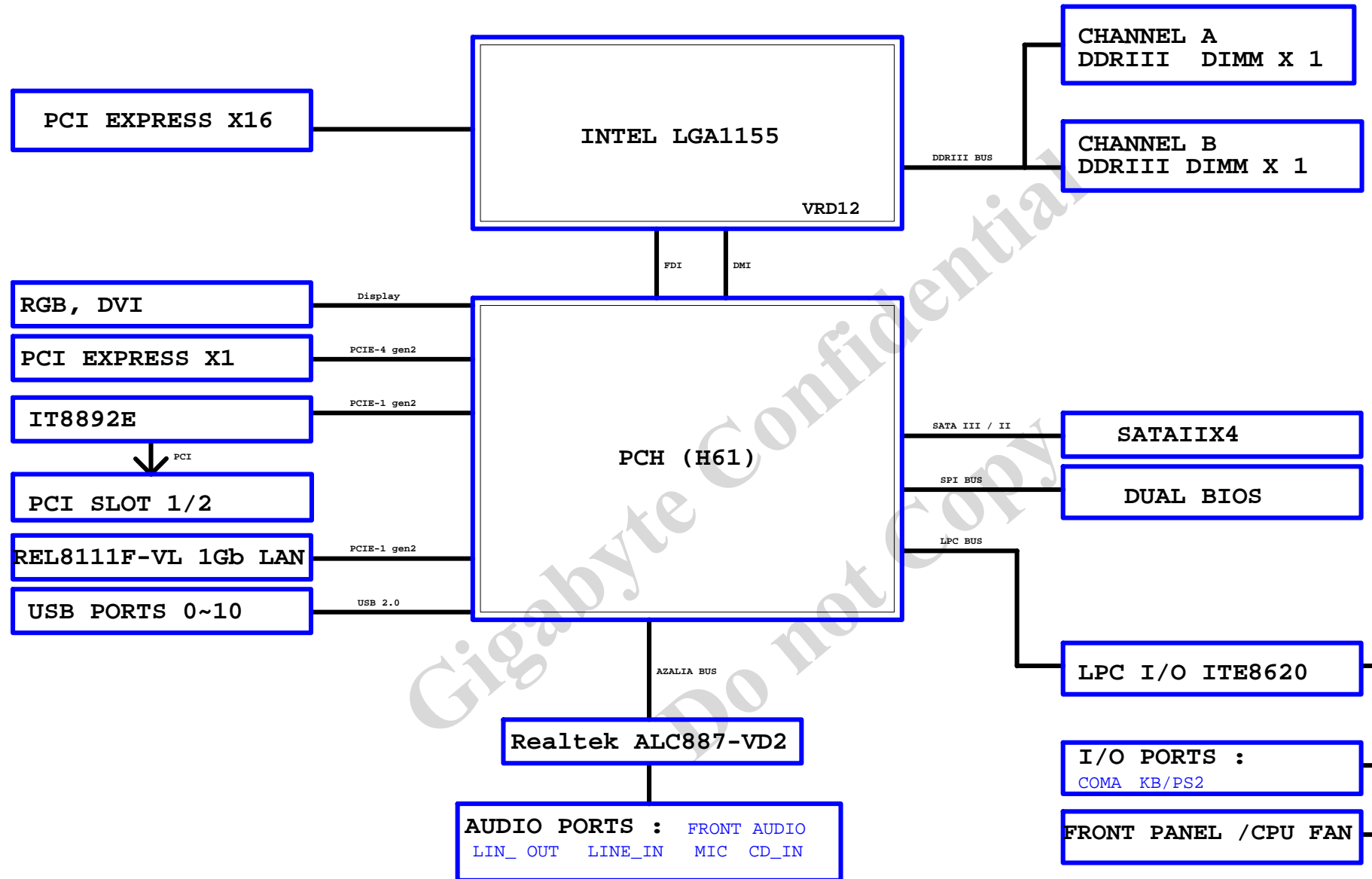
2014/01/13

[illegible]

## Circuit or PCB layout change

[illegible]

## BLOCK DIAGRAM

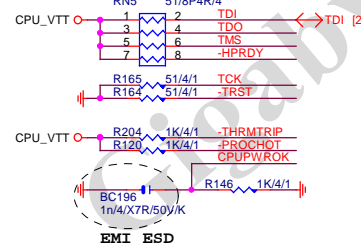




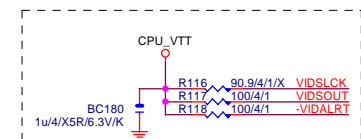
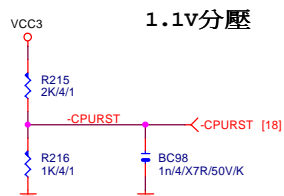
Stitching caps for PCIE,DMI,FDI bus



Title			
CPU LGA1155-A			
Size	Document Number		Rev
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### 1.1V分壓



## CPUA

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[3]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[4]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[5]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[6]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[7]	SA_DQ[3]	AJ2	MDA4
MAAA9	AT22	SA_MA[8]	SA_DQ[4]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
[7] -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
[7] -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
[7] -SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
			SA_DQ[11]	AR4	MDA12
[7] SBAA0	AY29	SA_BS[0]	SA_DQ[12]	AN2	MDA11
[7] SBAA1	AW28	SA_BS[1]	SA_DQ[13]	AN3	MDA13
[7] SBAA2	AV20	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
[7] -CSA0	AU29	SA_CS#	SA_DQS[2]	AW4	DQSA2
[7] -CSA1	AV32	SA_CS#	SA_DQS[2]	AW4	DQSA2
	AW30	SA_CS#	SA_DQS[2]	AW4	-DQSA2
	AW33	SA_CS#	SA_DQS[2]	AW4	-DQSA2
[7] CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
[7] CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
	AU18	SA_CKE[2]	SA_DQ[18]	AV5	MDA18
	AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
	MODT_A0	SA_ODT[0]	SA_DQ[21]	AU3	MDA21
	MODT_A1	SA_ODT[1]	SA_DQ[22]	AU5	MDA22
	AU30	SA_ODT[2]	SA_DQ[23]	AU5	MDA23
	AW33	SA_ODT[3]			
[7] DCLKA0	AY25	SA_CLK[0]	SA_DQS[3]	AV8	DQSA3
[7] -DCLKA0	AW25	SA_CLK[0]	SA_DQS[3]	AV8	-DQSA3
[7] DCLKA1	AU24	SA_CLK[1]			
[7] -DCLKA1	AU25	SA_CLK[1]	SA_DQ[24]	AY7	MDA24
	AW27	SA_CLK[2]	SA_DQ[25]	AU7	MDA25
	AW26	SA_CLK[3]	SA_DQ[26]	AV9	MDA26
	AW28	SA_CLK[3]	SA_DQ[27]	AU9	MDA27
			SA_DQ[28]	AV7	MDA28
			SA_DQ[29]	AW7	MDA29
			SA_DQ[30]	AW9	MDA30
			SA_DQ[31]	AY9	MDA31
			SA_DQS[4]	AV37	DQSA4
			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AU35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AU39	MDA34
			SA_DQ[35]	AU36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AU38	MDA38
			SA_DQ[39]	AU37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL39	MDA52
			SA_DQ[53]	AL38	MDA53
			SA_DQ[54]	AJ39	MDA54
			SA_DQ[55]	AJ40	MDA55
			SA_DQS[7]	AF38	DQSA7
			SA_DQS[7]	AF39	-DQSA7
			SA_DQ[56]	AG40	MDA56
			SA_DQ[57]	AG37	MDA57
			SA_DQ[58]	AE38	MDA58
			SA_DQ[59]	AE37	MDA59
			SA_DQ[60]	AG39	MDA60
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			SA_DQ[62]	AE40	MDA62
			SA_DQ[63]	AE40	MDA63

DDR\_0

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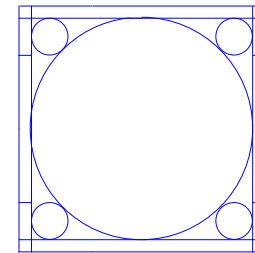
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## CPUB

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MAAB1	AM20	SB_MA[1]	SB_DQS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AY17	SB_MA[8]	SB_DQ[4]	AG6	MDB4
MAAB9	AN23	SB_MA[10]	SB_DQ[6]	AG6	MDB5
MAAB10	AN17	SB_MA[11]	SB_DQ[7]	AJ6	MDB6
MAAB11	AT18	SB_MA[12]			
MAAB12	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB13	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB14	AY16	SB_MA[15]			
MAAB15	AY16	SB_MA[15]			
[8] -SWEB	AR25	SB_WE#	SB_DQ[8]	AL7	MDB8
[8] -SCASB	AK25	SB_CAS#	SB_DQ[9]	AM7	MDB9
[8] -SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MDB10
			SB_DQ[11]	AL10	MDB11
[8] SBAB0	AP23	SB_BS[0]	SB_DQ[12]	AL2	MDB12
[8] SBAB1	AM24	SB_BS[1]	SB_DQ[13]	AM6	MDB13
[8] SBAB2	AW17	SB_BS[2]	SB_DQ[14]	AL9	MDB14
			SB_DQ[15]	AM9	MDB15
[8] -CSB0	AN25	SB_CS#	SB_DQS[2]	AR8	DQSB2
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[8] CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
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	AV15	SB_CKE[3]	SB_DQ[19]	AR10	MDB19
			SB_DQ[20]	AP6	MDB20
	MODT_B0	SB_ODT[0]	SB_DQ[21]	AR6	MDB21
	MODT_B1	SB_ODT[1]	SB_DQ[22]	AP9	MDB22
	AM26	SB_ODT[2]	SB_DQ[23]	AR9	MDB23
	AK26	SB_ODT[3]			
			SB_DQS[3]	AN13	DQSB3
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[8] DCLKB0	AL21	SB_CLK[0]	SB_DQ[24]	AM12	MDB24
[8] -DCLKB0	AL22	SB_CLK[0]	SB_DQ[25]	AM13	MDB25
[8] DCLKB1	AL20	SB_CLK[1]	SB_DQ[26]	AR13	MDB26
[8] -DCLKB1	AK20	SB_CLK[1]	SB_DQ[27]	AP13	MDB27
	AL23	SB_CLK[2]	SB_DQ[28]	AL12	MDB28
	AM22	SB_CLK[2]	SB_DQ[29]	AL13	MDB29
	AK21	SB_CLK[3]	SB_DQ[30]	AR12	MDB30
	AN21	SB_CLK[3]	SB_DQ[31]	AP12	MDB31
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VREF_DQB	AH1	FC_AH1			
VREF_DQA	AH4	FC_AH4			
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			SB_DQ[35]	AL29	MDB35
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			SB_DQ[39]	AM29	MDB39
			SB_DQS[5]	AP33	DQSB5
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			SB_ECC_CB[1]	AP31	MDB41
			SB_ECC_CB[2]	AP35	MDB42
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			SB_DQ[51]	AM35	MDB54
			SB_DQ[52]	AL34	MDB55
			SB_DQ[53]	AG35	DQSB7
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			SB_DQ[55]		
			SB_DQ[56]	AH35	MDB56
			SB_DQ[57]	AH34	MDB57
			SB_DQ[58]	AE34	MDB58
			SB_DQ[59]	AE35	MDB59
			SB_DQ[60]	AJ35	MDB60
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			SB_DQ[62]	AF33	MDB62
			SB_DQ[63]	AF35	MDB63

DDR\_1

LGA1155[10SC1-F01155-23R\_10SC1-F01155-22R]

CR  
CPU RETENTION/X

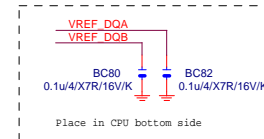
Need check the new CPU ME

CPU\_P



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## DDR SIGNAL



[7] MODT\_A[0..1] &lt;-- MODT\_A[0..1]

[8] MODT\_B[0..1] &lt;-- MODT\_B[0..1]

[7] MDA[0..63] &lt;-- MDA[0..63]

[8] MDB[0..63] &lt;-- MDB[0..63]

[7] DQSA[0..7] &lt;-- DQSA[0..7]

[7] -DQSA[0..7] &lt;-- -DQSA[0..7]

[7] MAA[A0..15] &lt;-- MAA[A0..15]

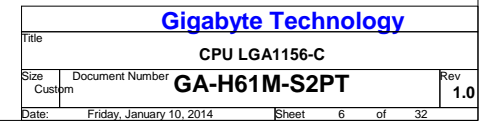
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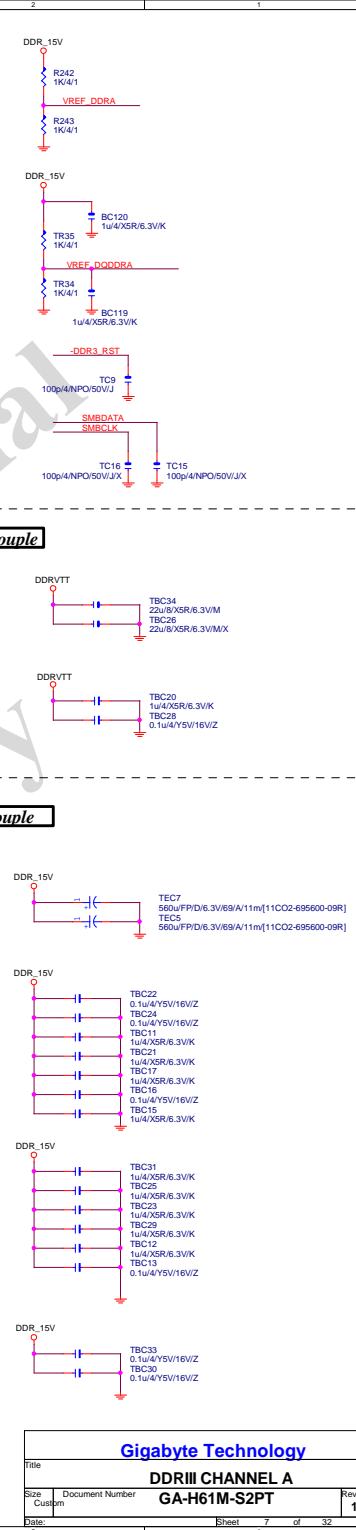
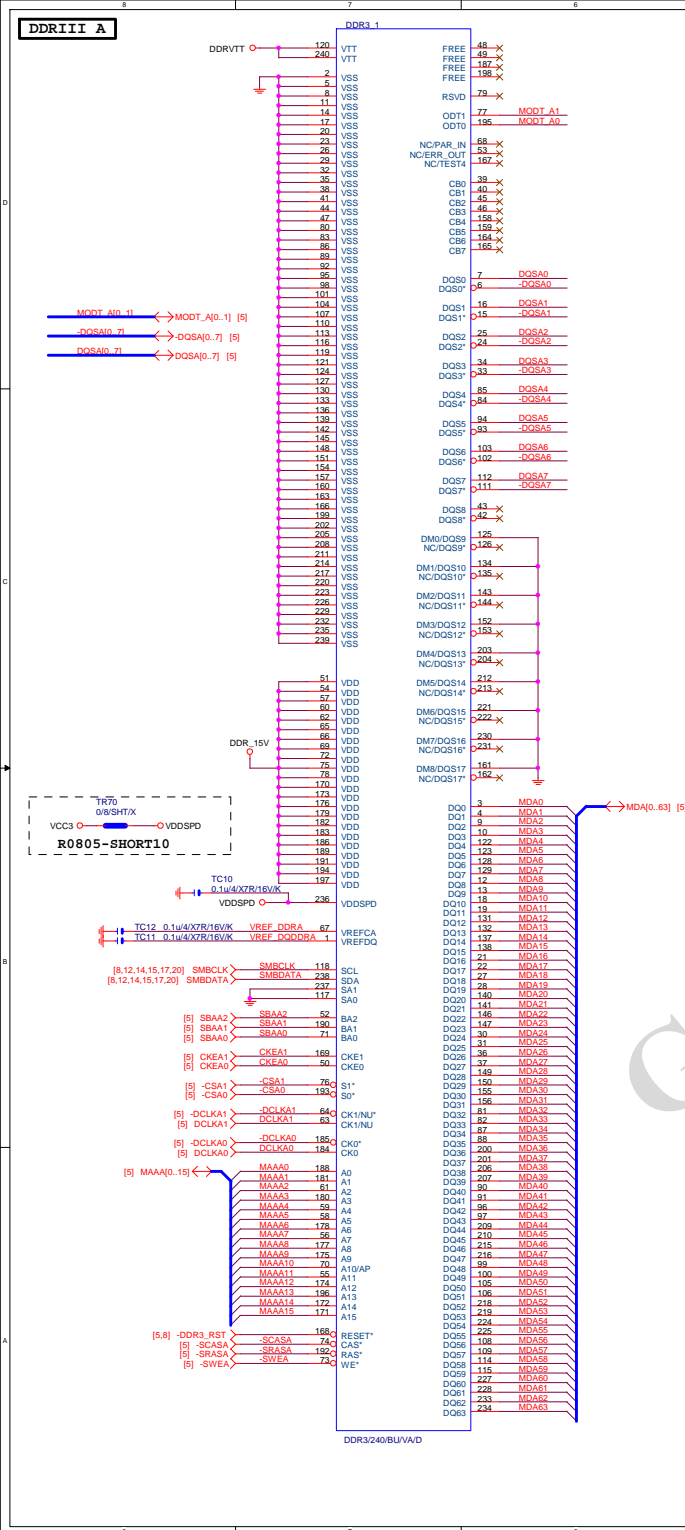
[8] DQSB[0..7] &lt;-- DQSB[0..7]

[8] -DQSB[0..7] &lt;-- -DQSB[0..7]

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Title			
CPU LGA1156-B			
Size	Document Number	Rev	
Custom	GA-H61M-S2PT	1.0	
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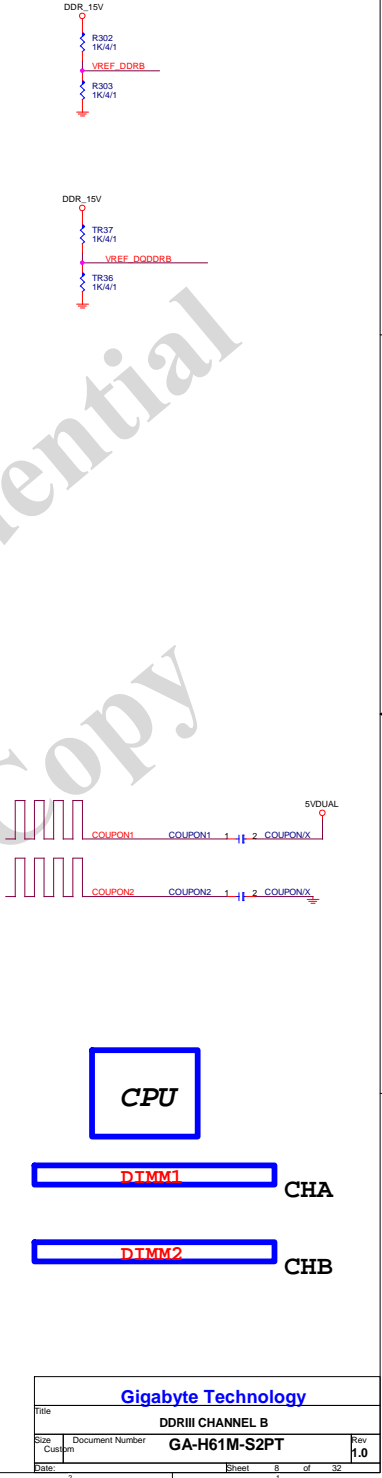
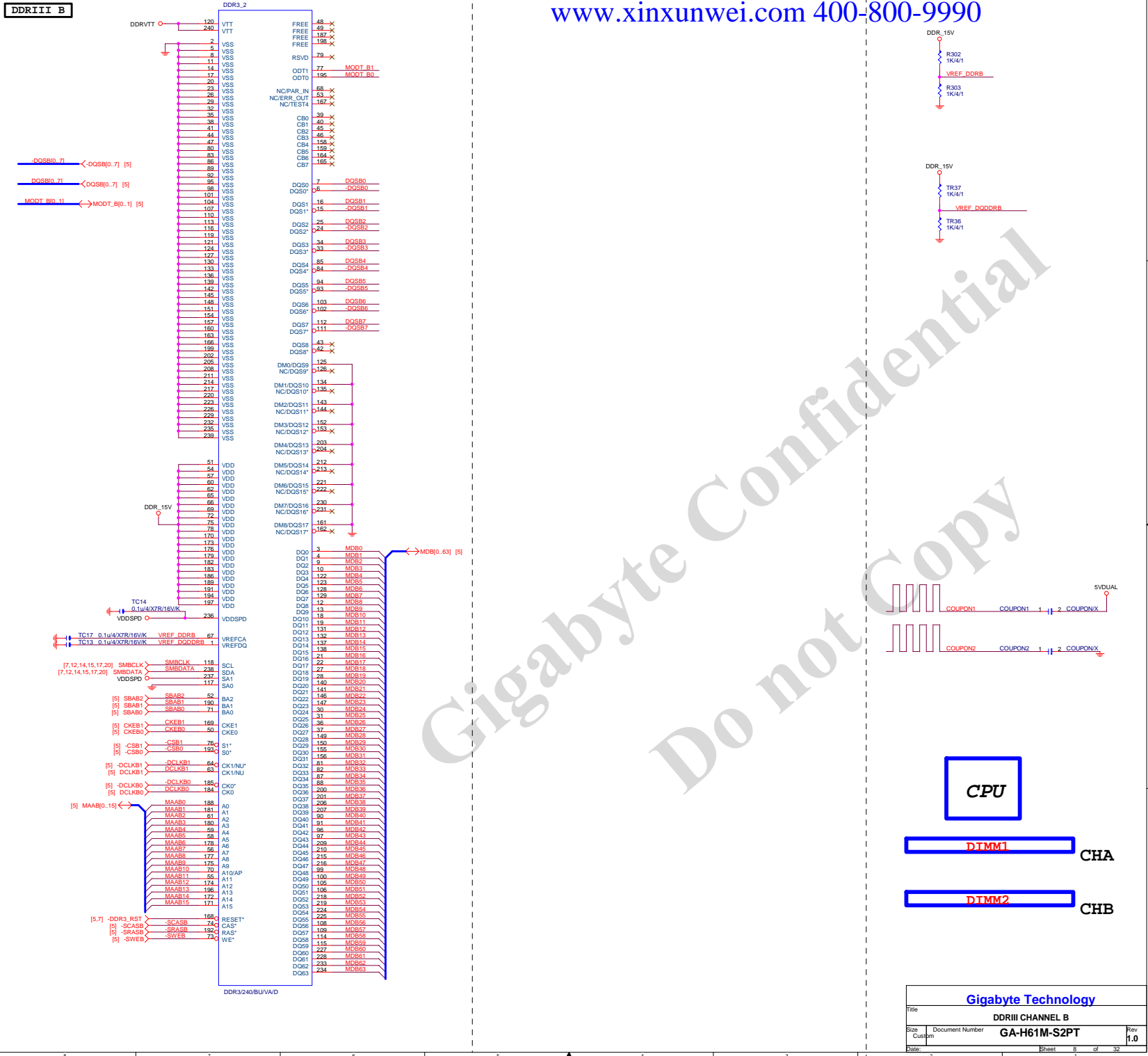




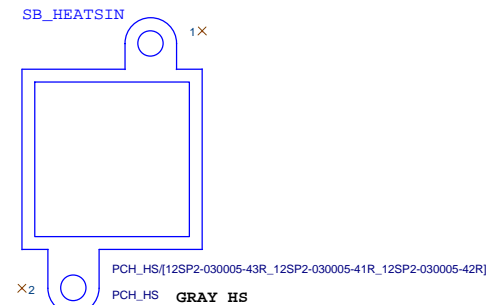
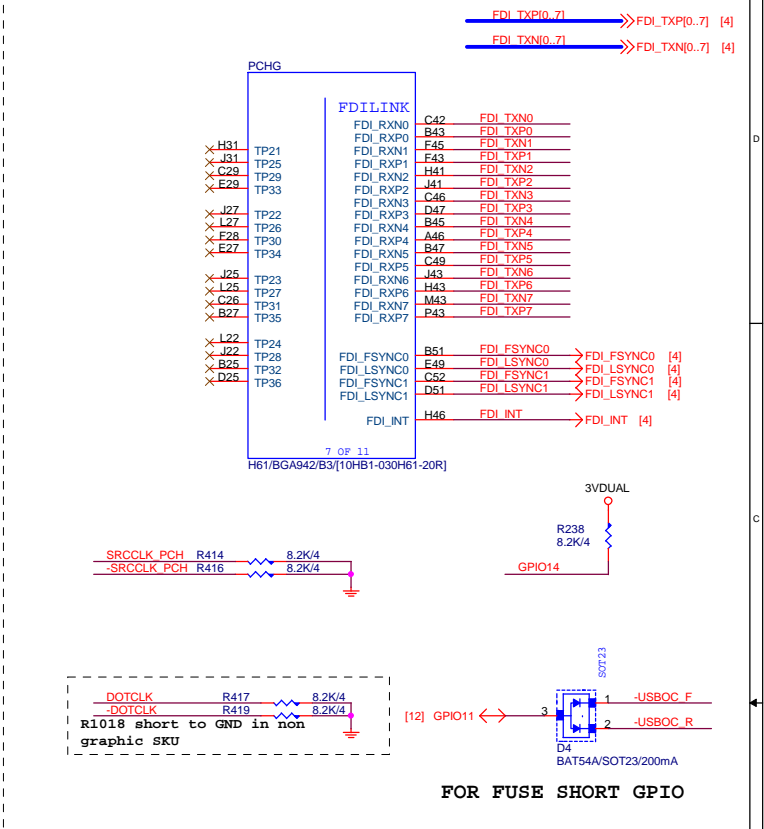


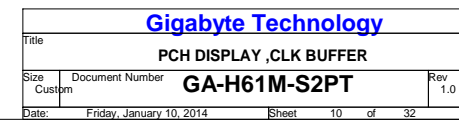
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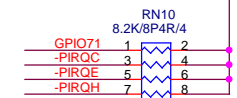
www.xinxunwei.com 400-800-9990

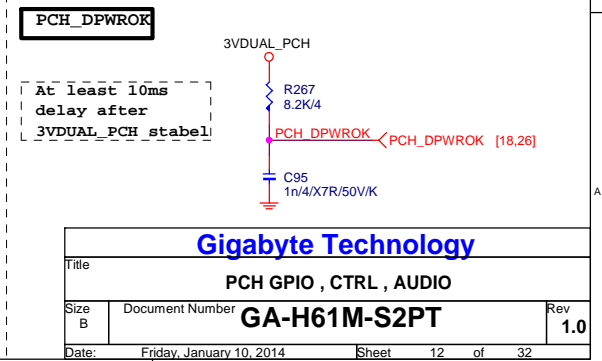
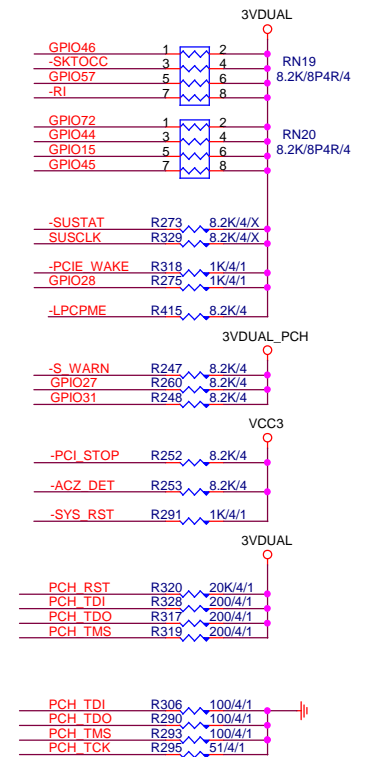




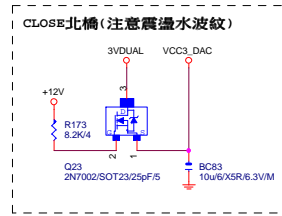
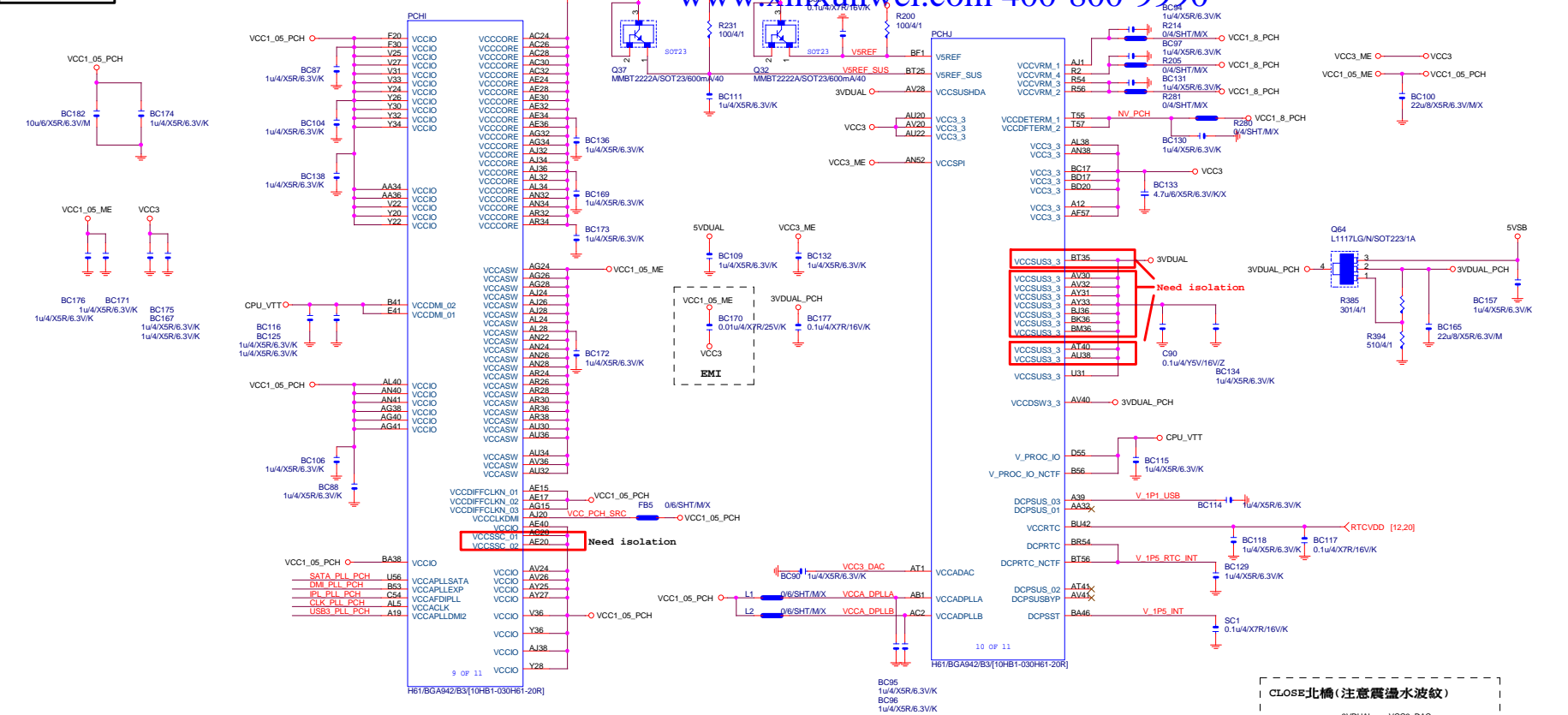




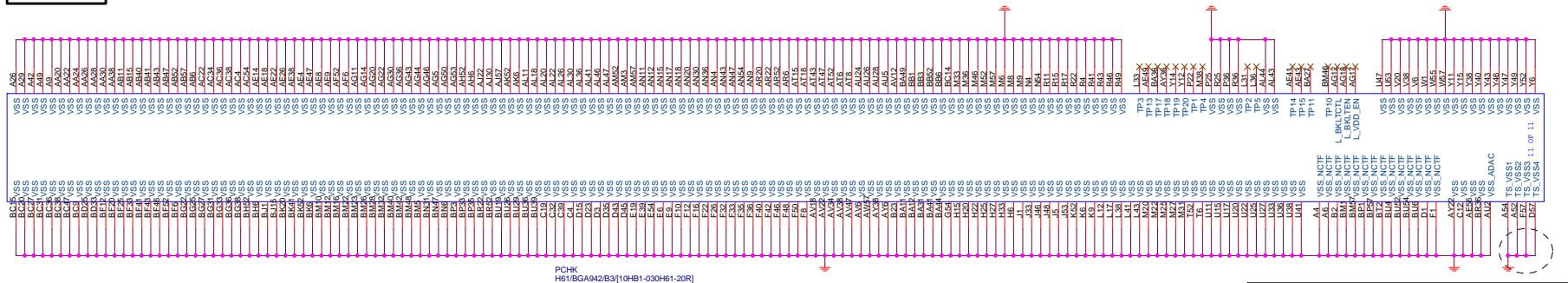




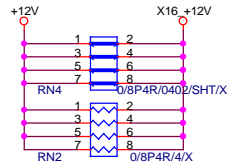
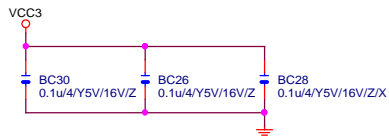
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PCH K GND

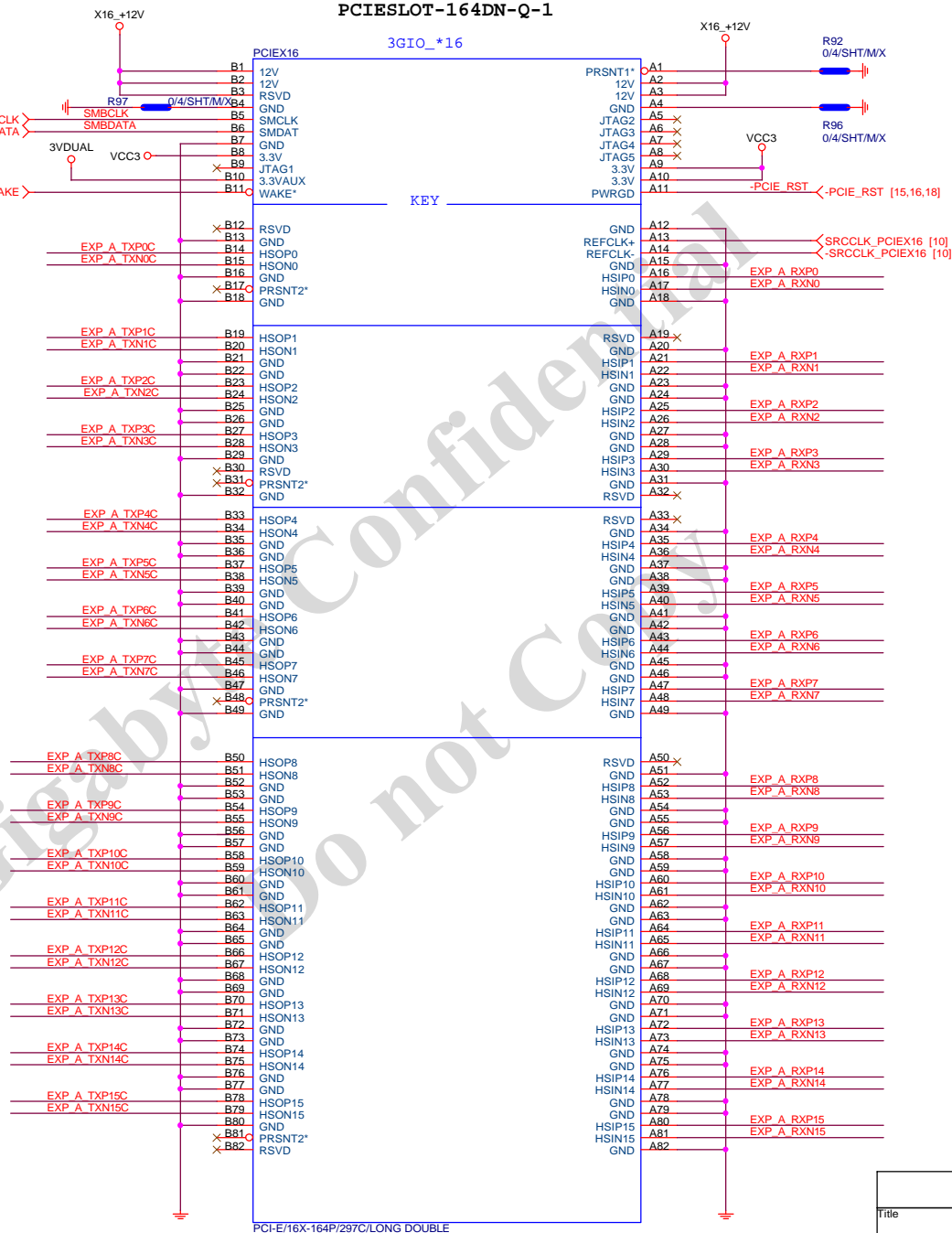


## PCIEX16



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 EXP A RXN0..15] >> EXP\_A\_RXN[0..15] [4]  
 EXP A TXP0..15] >> EXP\_A\_TXP[0..15] [4]  
 EXP A TXN0..15] >> EXP\_A\_TXN[0..15] [4]

EXP A TXP0	C32	0.22u/4/X5R/6.3V/K	EXP A TXP0C
EXP A TXN0	C30	0.22u/4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C35	0.22u/4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C37	0.22u/4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C39	0.22u/4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C41	0.22u/4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C43	0.22u/4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C45	0.22u/4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C46	0.22u/4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C49	0.22u/4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C50	0.22u/4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C51	0.22u/4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C52	0.22u/4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C54	0.22u/4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C57	0.22u/4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C58	0.22u/4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C60	0.22u/4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C61	0.22u/4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C62	0.22u/4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C63	0.22u/4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C64	0.22u/4/X5R/6.3V/K	EXP A TXP10C
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EXP A TXP11	C66	0.22u/4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C67	0.22u/4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C68	0.22u/4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C70	0.22u/4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C72	0.22u/4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C73	0.22u/4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C74	0.22u/4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C75	0.22u/4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C77	0.22u/4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C78	0.22u/4/X5R/6.3V/K	EXP A TXN15C



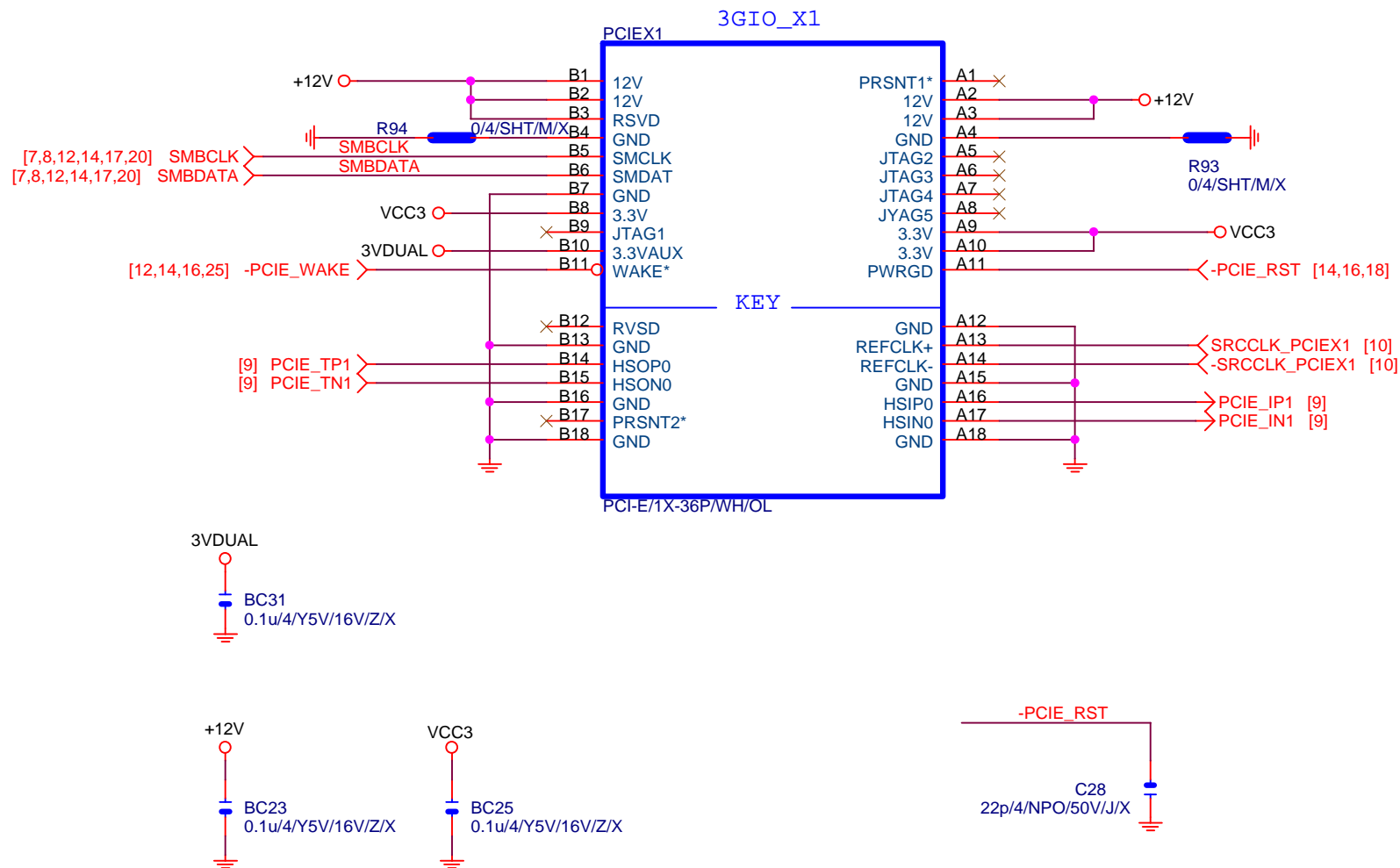
PCI-E/16X-164P/297C/LONG DOUBLE

LONG DOUBLE PUSH LATCH

Gigabyte Technology

Title			
PCI EXPRESS * 16			
Size			
Custom			
Document Number			
GA-H61M-S2PT			
Rev			
1.0			
Date:			
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**Gigabyte Technology**

Title

**PCI EXPRESS X 1 PORT**Size  
A

Document Number

**GA-H61M-S2PT**Rev  
**1.0**

Date: Friday, January 10, 2014

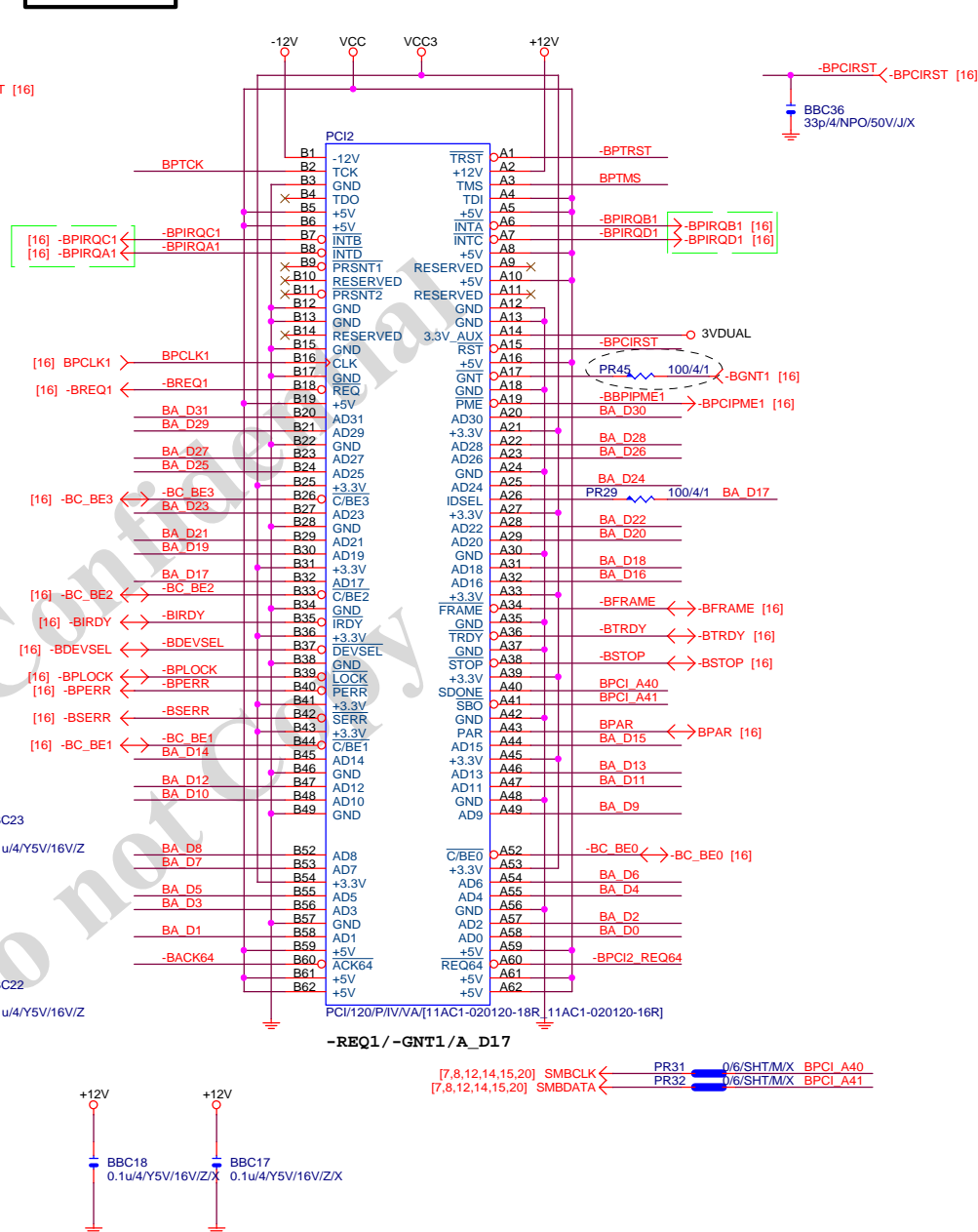
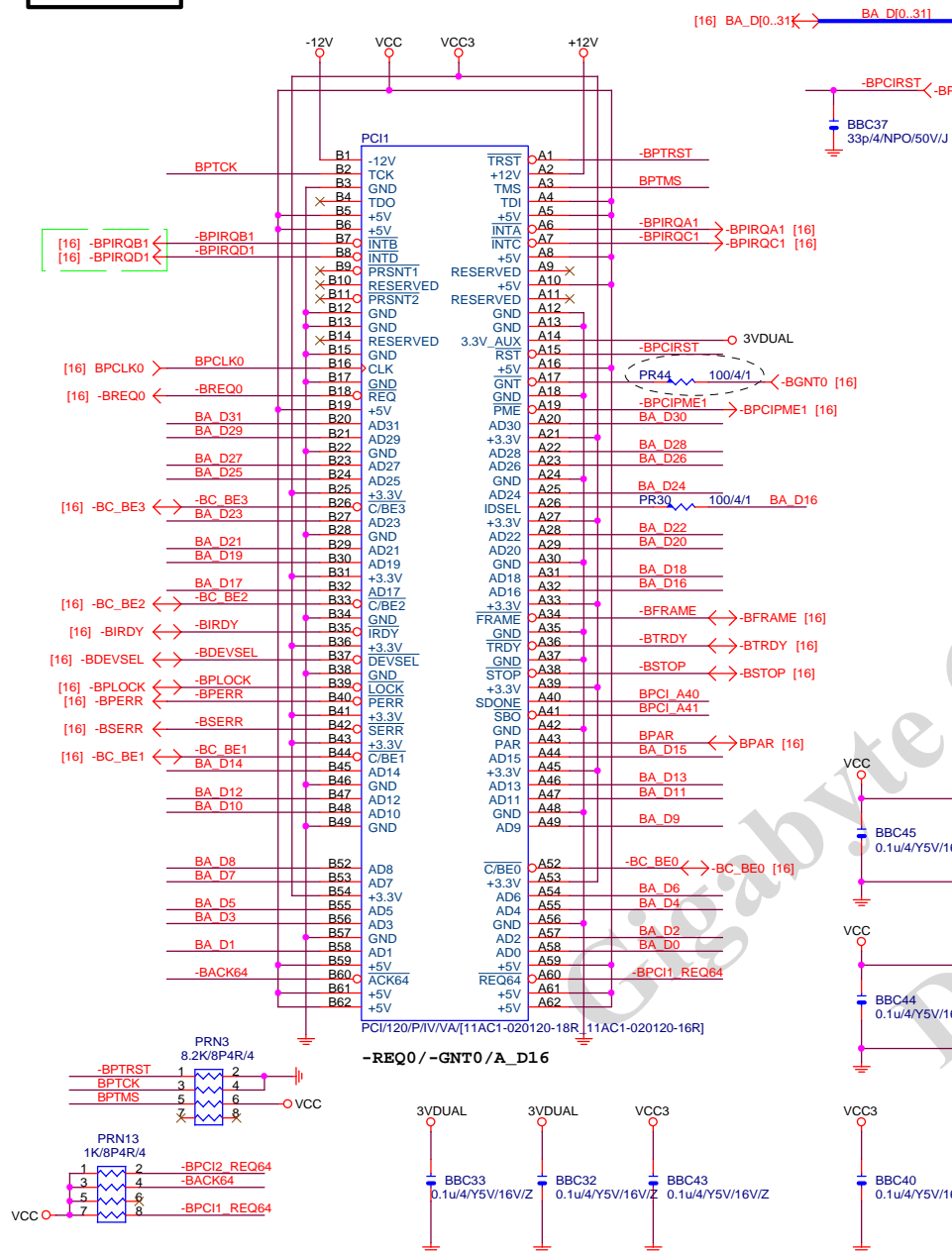
Sheet 15 of 32





PCI SLOT 1

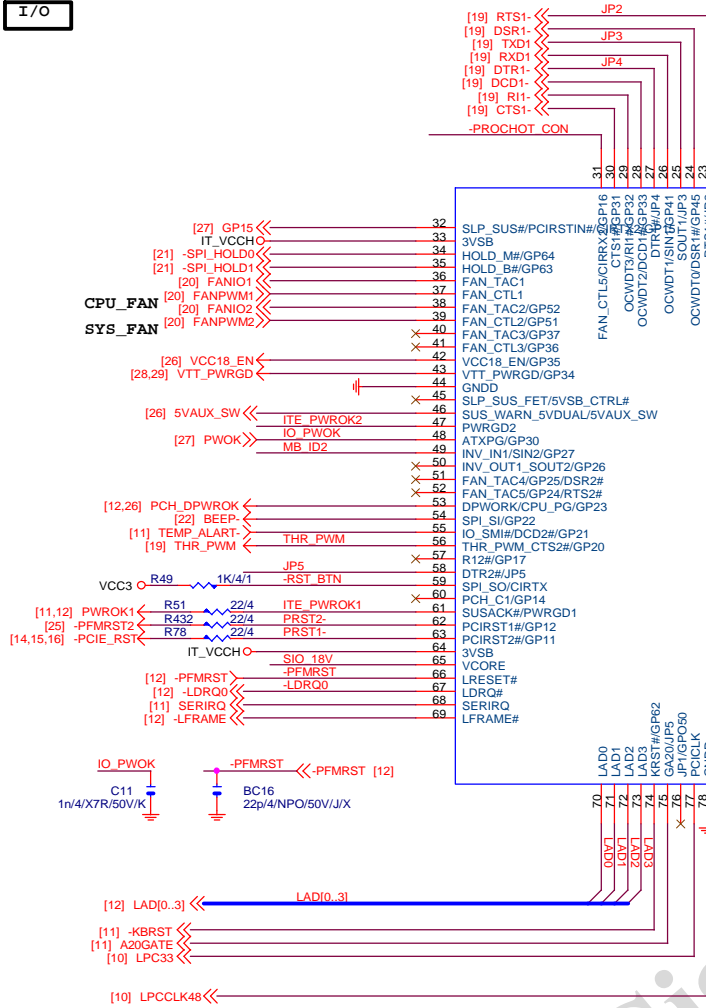
PCI SLOT 2



Gigabyte Technology

Title		
PCI SLOT 1&2		
Size	Document Number	Rev
Custom	GA-H61M-S2PT	1.0
Date:	Friday, January 10, 2014	Sheet 17 of 32

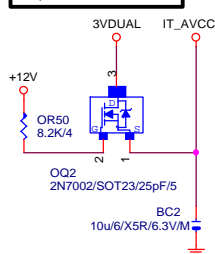
## I/O



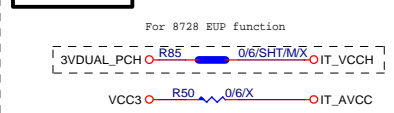
## IT8620E

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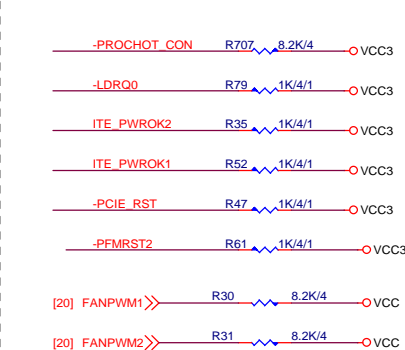
## I/O LEAKAGE



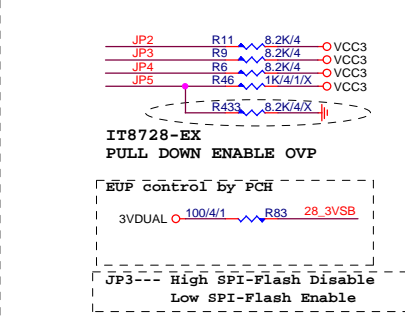
## PWR SHT



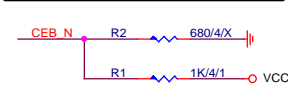
## SIO PU



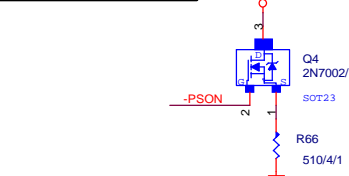
## SIO PU



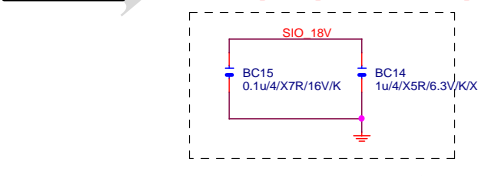
## DUAL BIOS OPT STRAP



## Power leakage

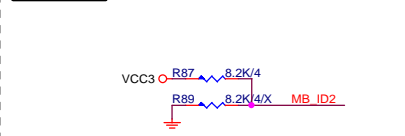


## SIO\_18V

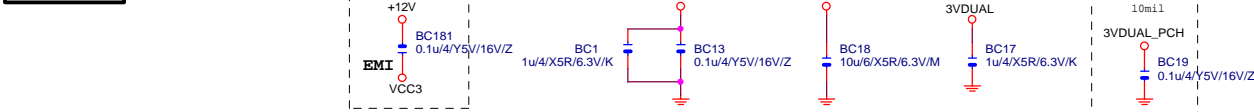


FOR LOW TEMP POWER ON INTO TEST MODE ISSUE

## MB\_ID



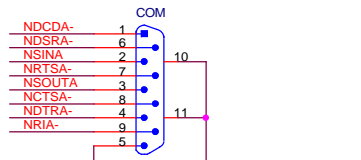
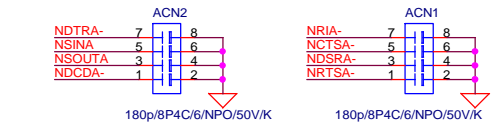
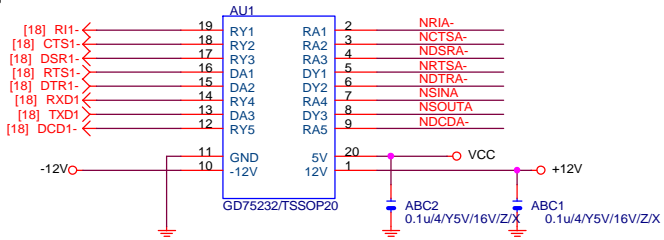
## SIO\_CAP



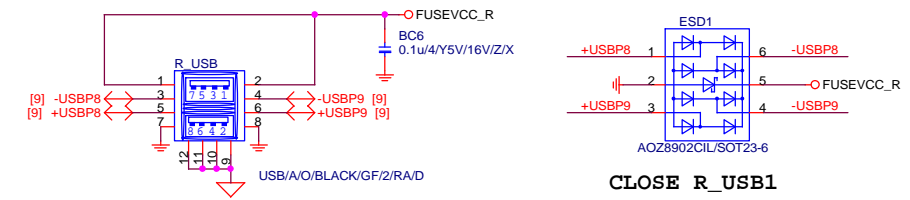
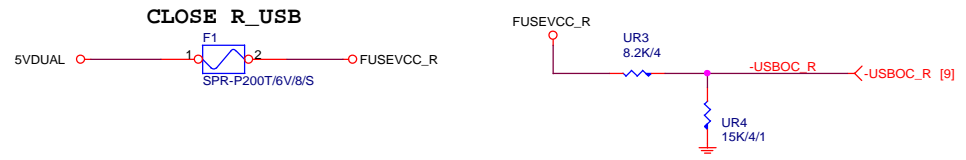
## Gigabyte Technology

Title			ITE 620 LPC IO
Size			GA-H61M-S2PT
Date:			Friday, January 10, 2014
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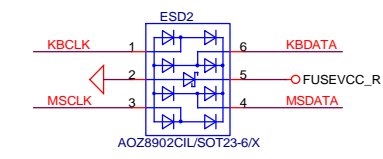
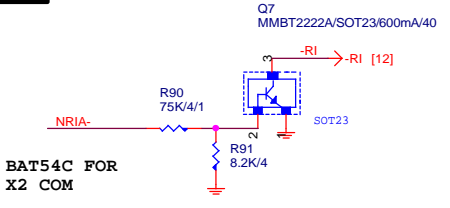
COM



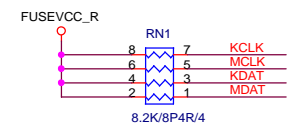
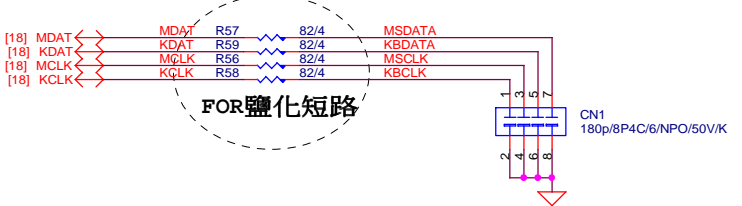
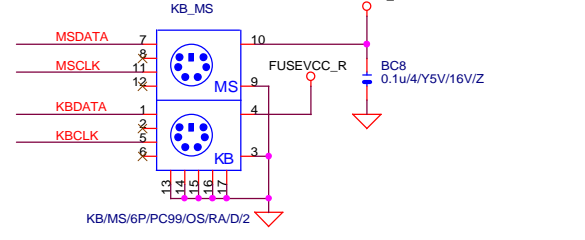
R\_USB



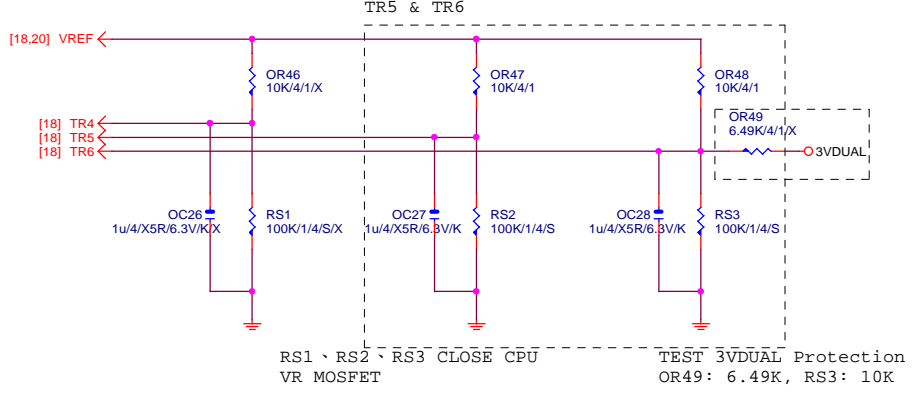
KB\_MS



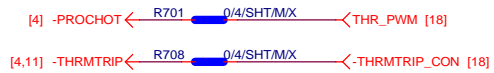
CLOSE KB\_MS



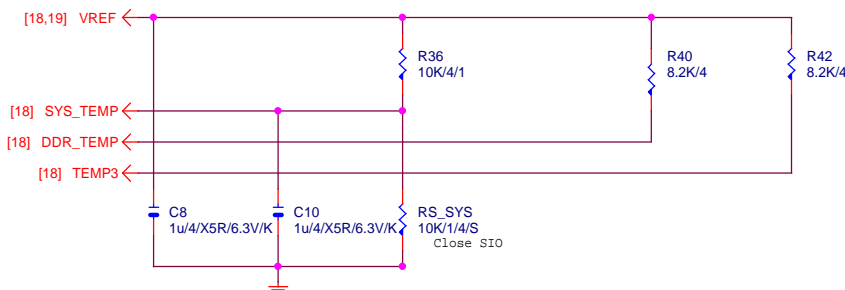
THERMAL SENSOR



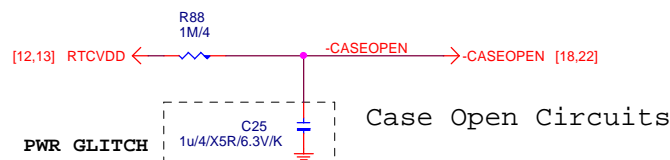
-PROHOT & THERMALTRIP



# TEMP H/W MONITOR

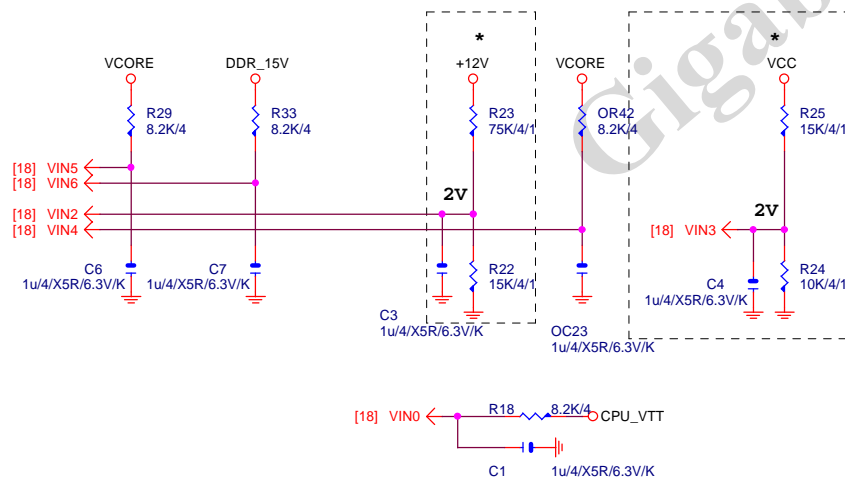


# CASE OPEN

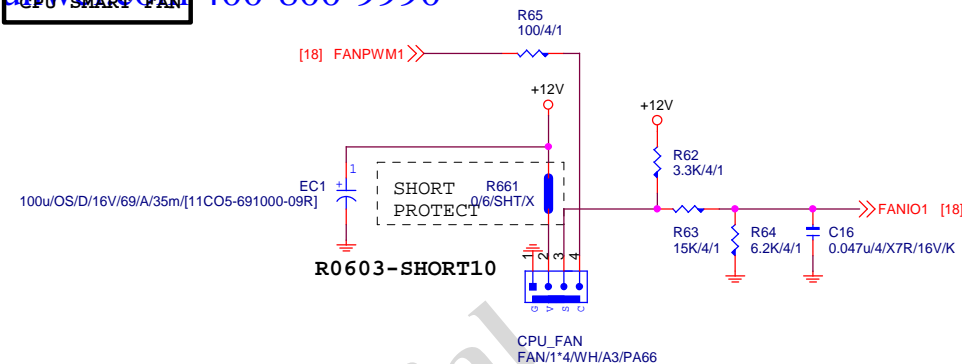


# VOLTAGE-- H/W MONITOR

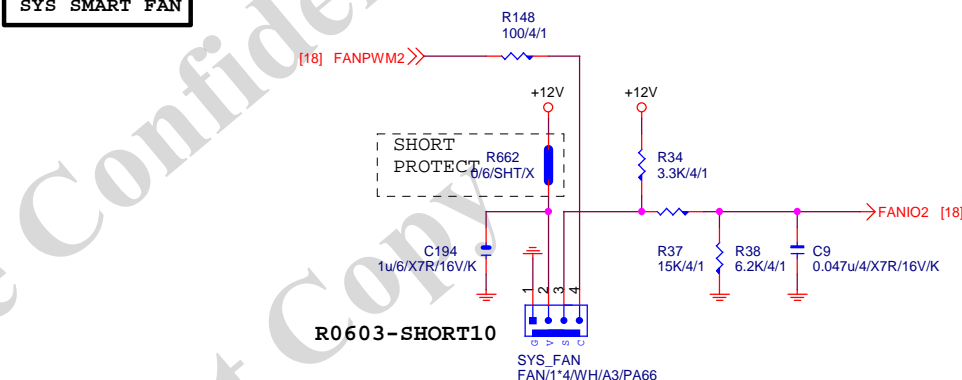
IT8728/EX VIN2/VIN3-->2V



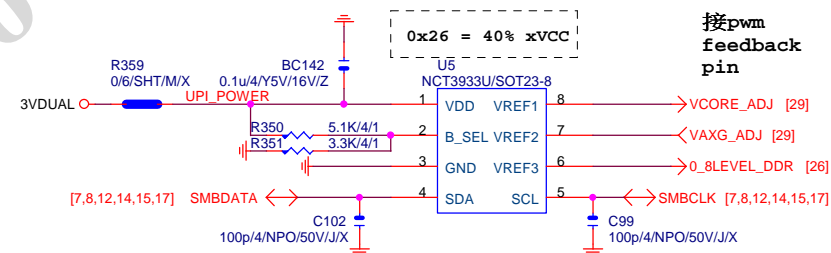
# CPU SMART FAN



# SYS SMART FAN

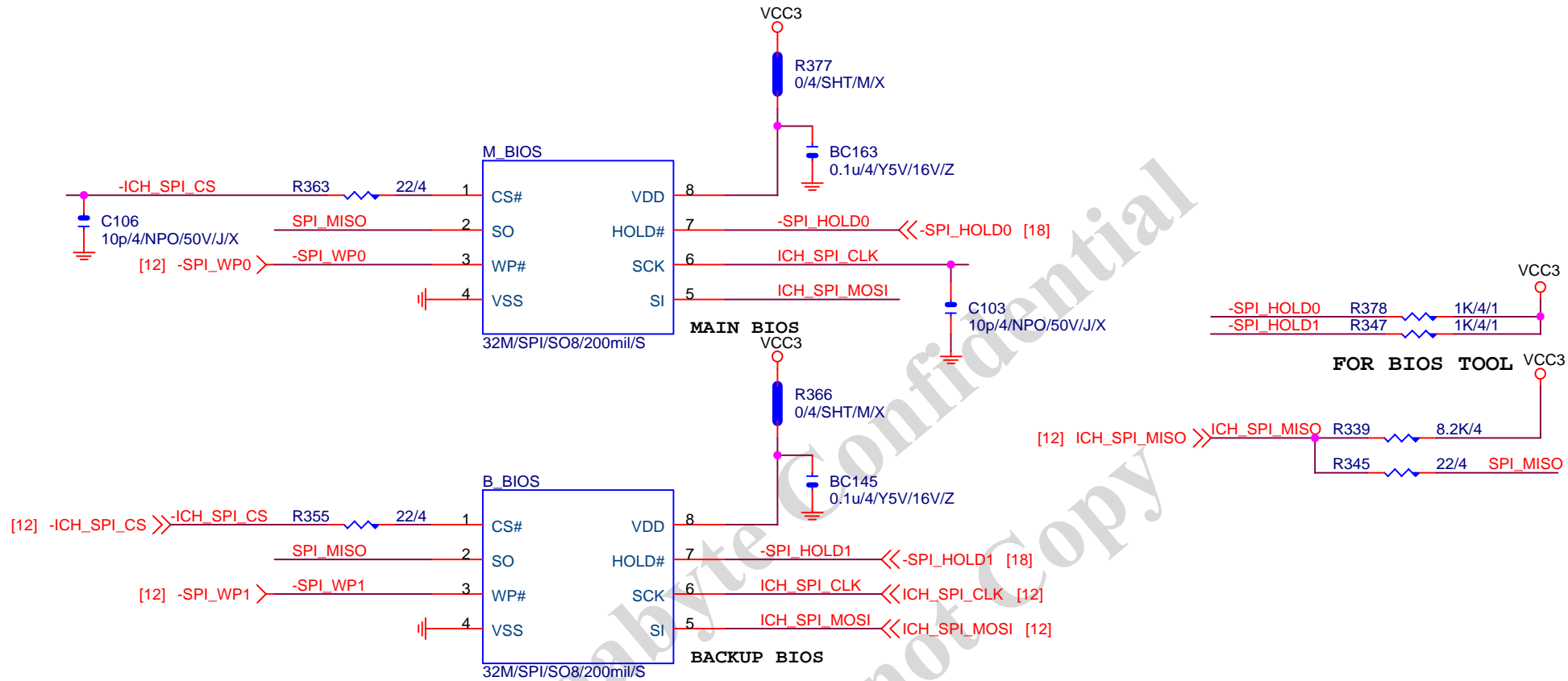


# O.V.



Gigabyte Technology

Title		HWM,FAN CTRL,OV	
Size	Custom	Document Number	GA-H61M-S2PT
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**DUAL BIOS**

B65使用64M BIOS

使用H67暫用32M

H61使用32M BIOS

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating  
0 means PD 1K

**Gigabyte Technology**

Title

**DUAL BIOS**

Size  
A

Document Number

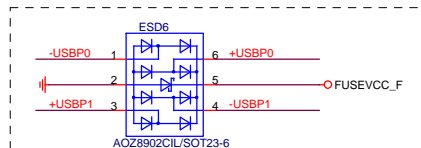
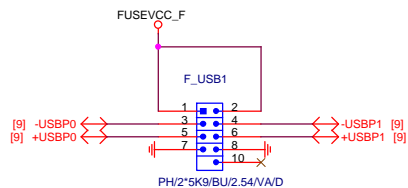
**GA-H61M-S2PT**

Rev  
**1.0**

Date: Friday, January 10, 2014

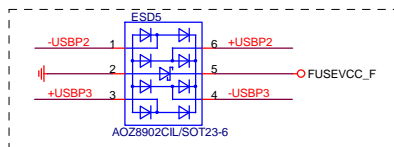
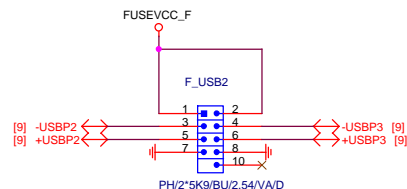
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# FRONT USB1



Close to connector

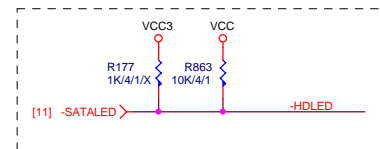
# FRONT USB2



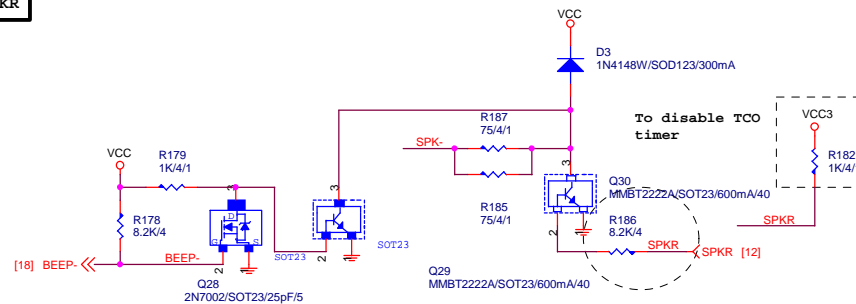
Close to connector

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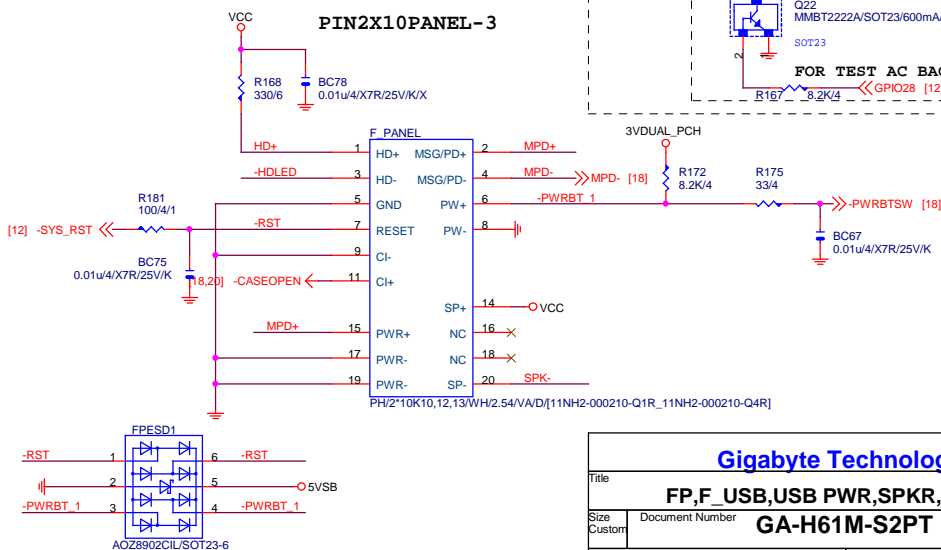
# SATA LED



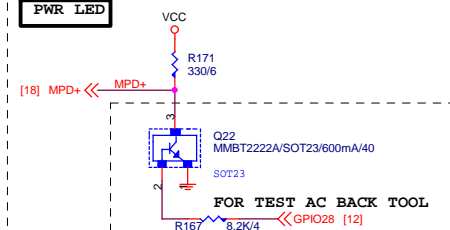
# SPKR



# INTEL FRONT PANEL



# PWR LED



Gigabyte Technology

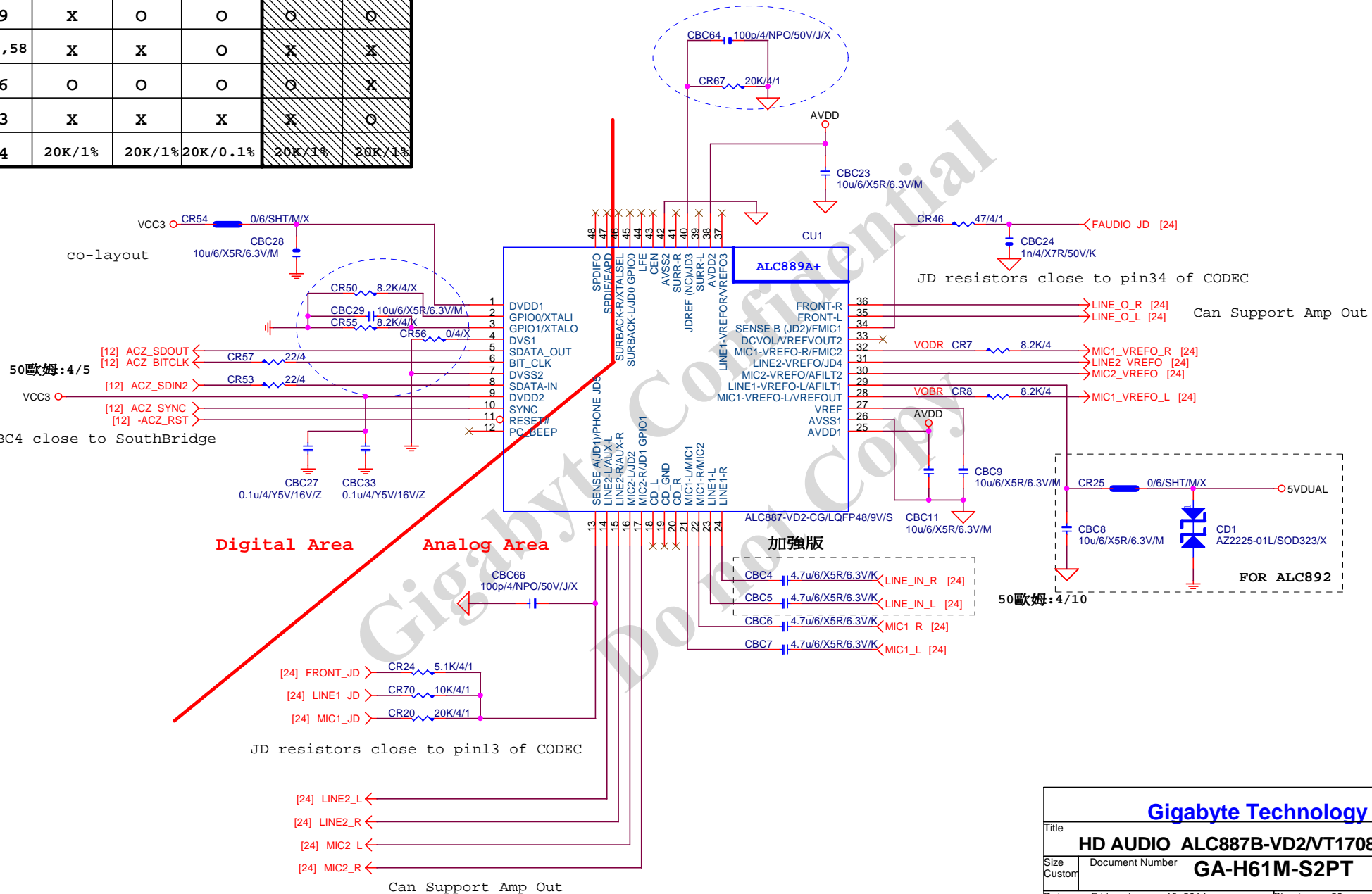
Title			FP,F_USB,USB PWR,SPKR,SATA LED
Size	Document Number	GA-H61M-S2PT	
Custom		Rev	1.0
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## AZALIA CODEC

ALC892/ALC889A/ALC889/ALC888B Colay

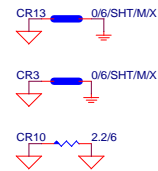
	ALC888B	ALC888-VA	ALC889A	ALC888-VD	ALC892
CR59	X	O	O	O	O
CR53,58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%



Gigabyte Technology

Title	HD AUDIO ALC887B-VD2/VT1708S/VT2021		
Size	Document Number	GA-H61M-S2PT	
Custom		Rev	1.0
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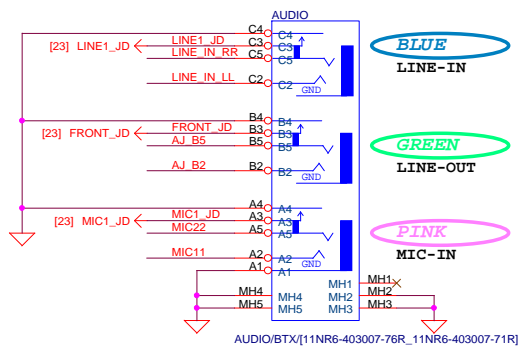
## CODEC POWER/EMI PAD



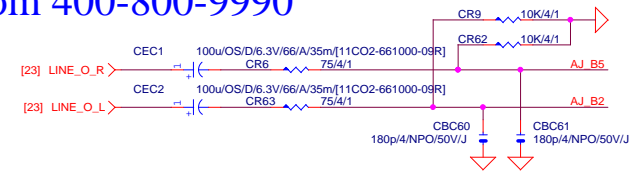
## SPDIF

N/A

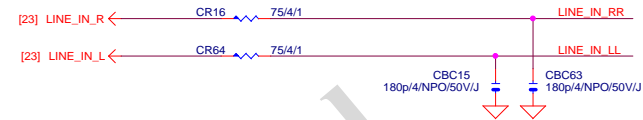
## AZALIA JACK



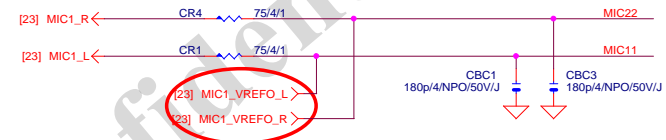
## LINE-OUT



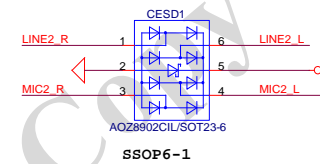
## LINE-IN



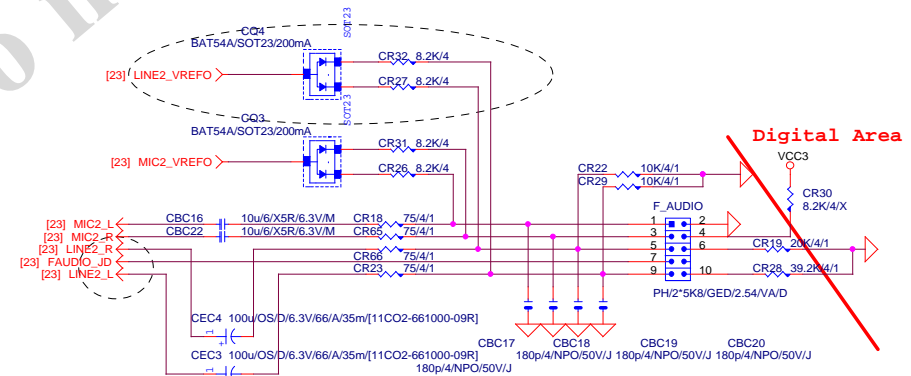
## MIC-IN



## F\_AUDIOESD

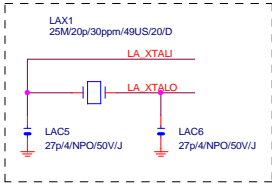
ALC892: X  
ALC887: O

## AZALIA FRONT PANEL

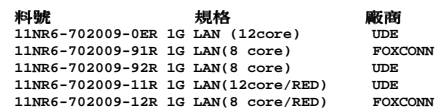
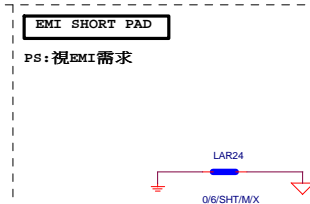
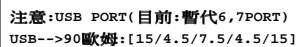
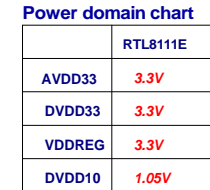


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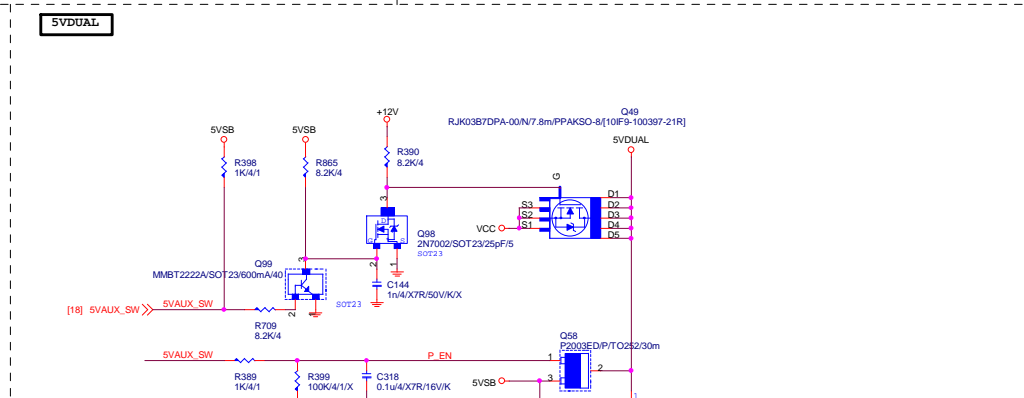
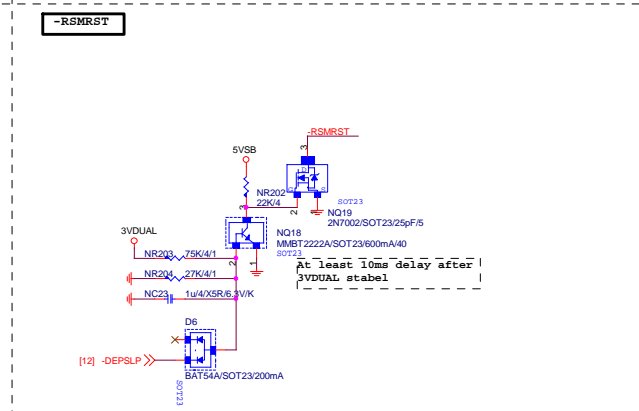
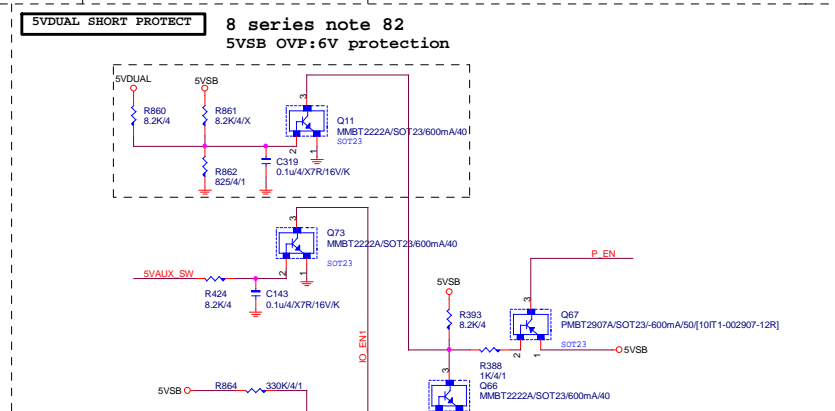
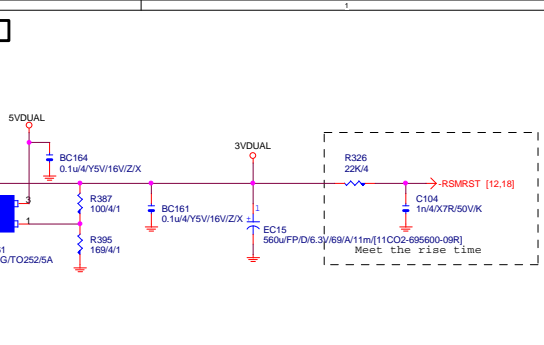
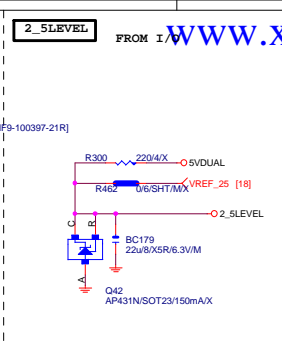
Title			
AUDIO JACK			
Size	Document Number		Rev
Custom	GA-H61M-S2PT		1.0
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SRCCLK-->50歐姆:[18/4/10/4/18]

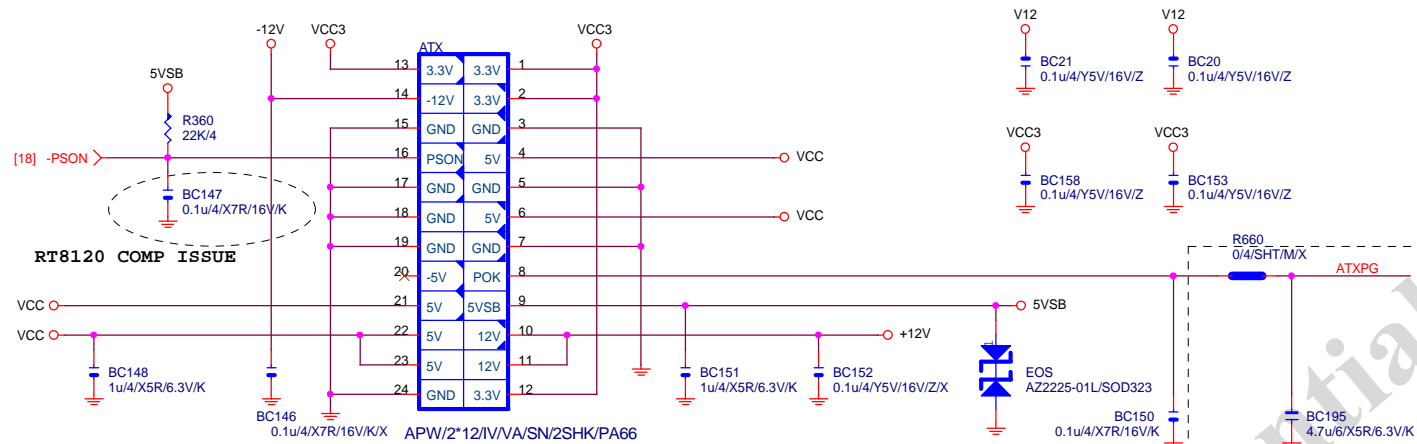


1. (紅色/12CORE/三倍):USB+LAN/1G/GO,Y/OS/RA/D/1/REI
2. (黑色/12CORE):USB+LAN/1G/GO,Y/OS/RA/D/1
3. (黑色/8CORE):USB+LAN/1G/GO,Y/OS/RA/D/8C

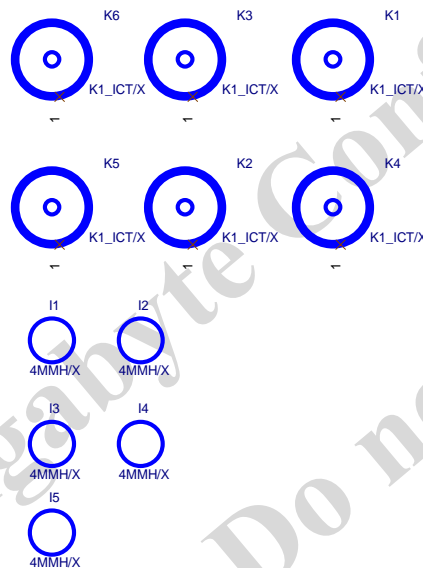
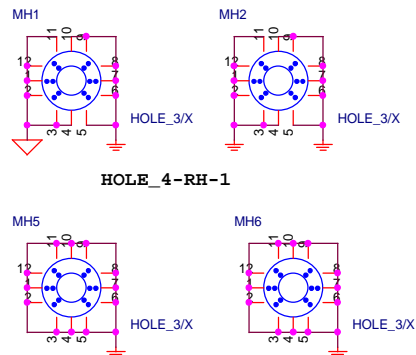


# ATXX24 POWER CONNECTOR

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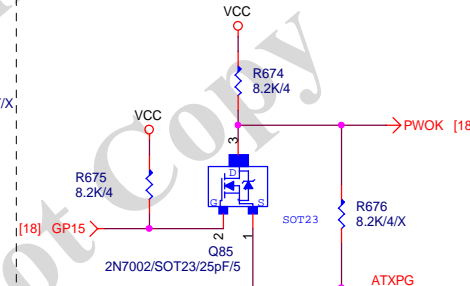


To prevent the 5VSB under loading when boot

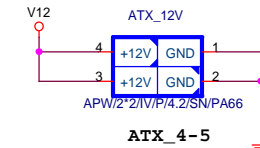


## PWOK PATCH

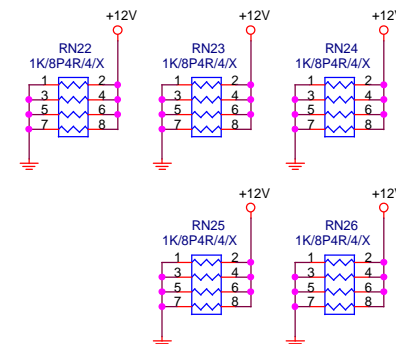
【技術通報R&D技術通報154】



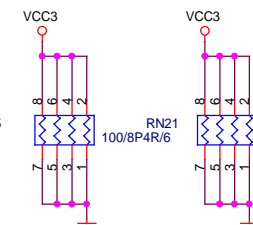
# ATXX4 POWER CONNECTOR



## MIN. LOAD



## FIX PWR MINMUN LOAD

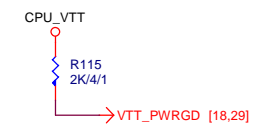
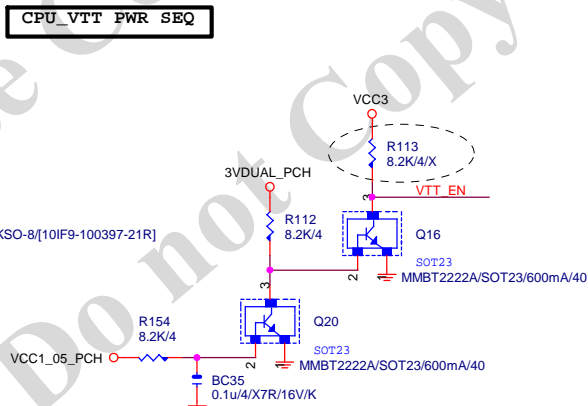
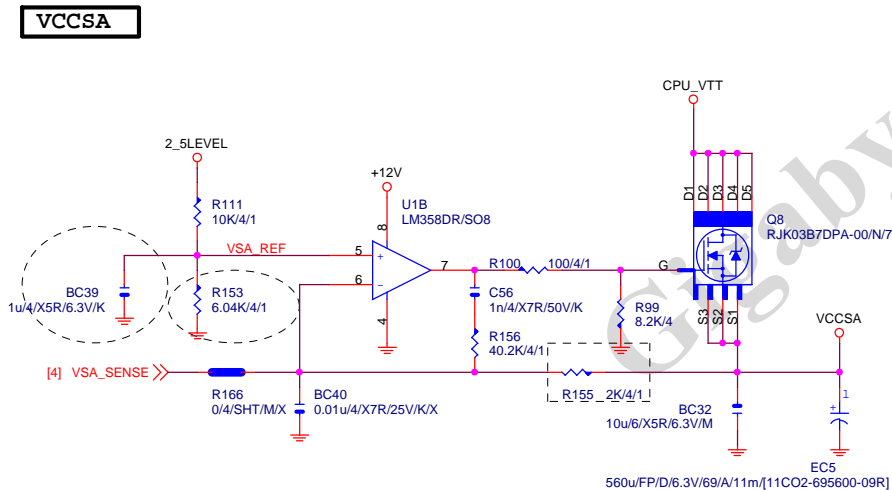
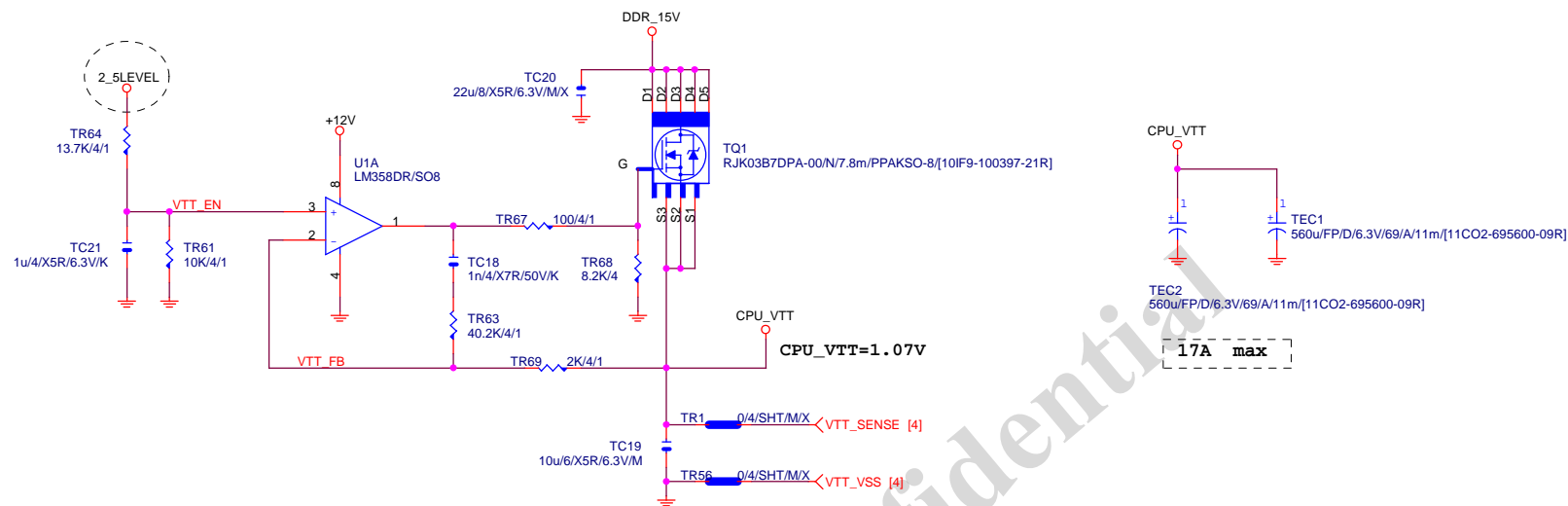


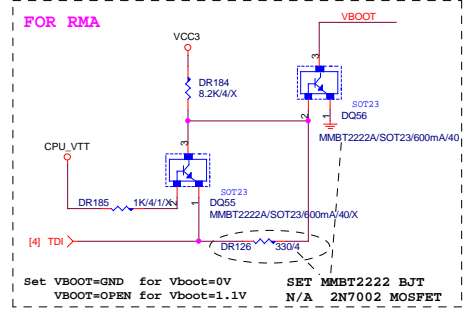
Gigabyte Technology

## ATX CONNECTOR

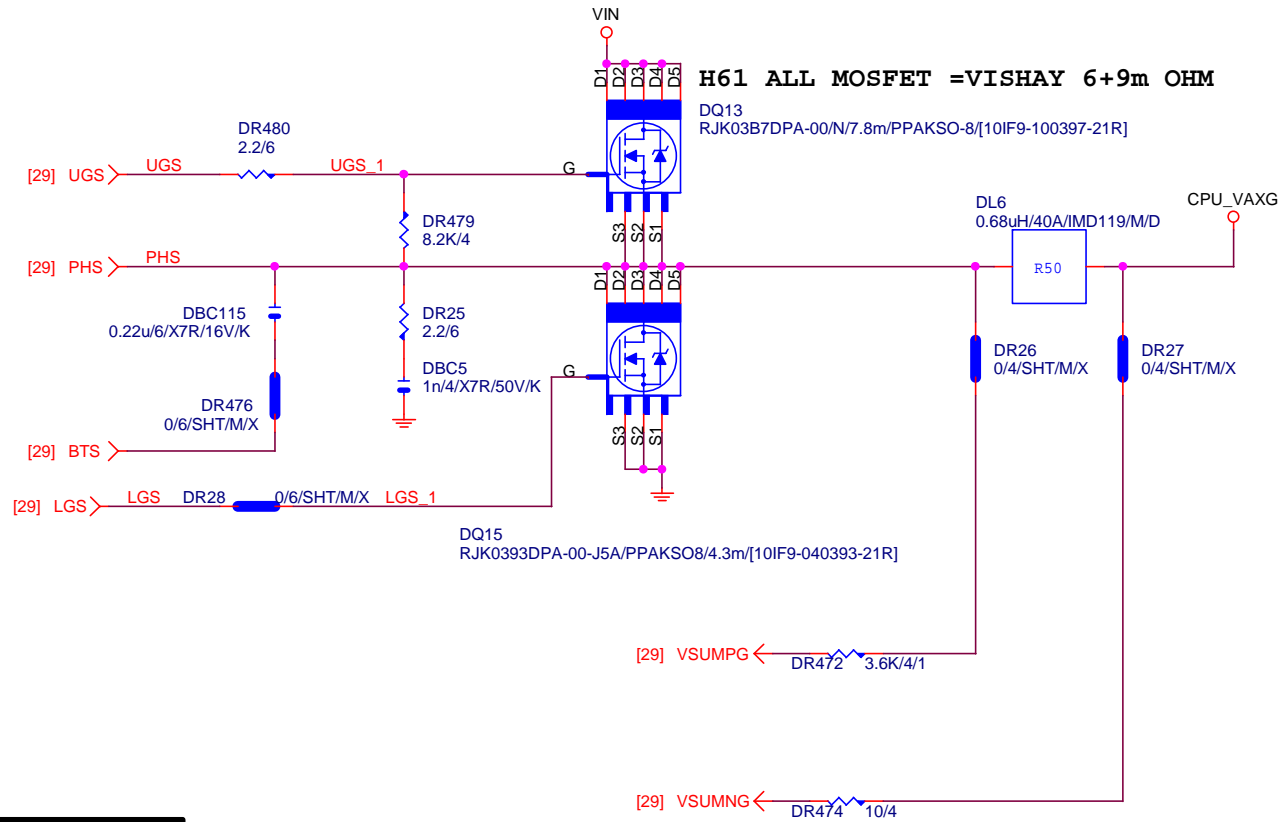
GA-H61M-S2PT

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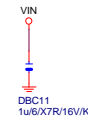
**VAXG****MOS HEATSINK**

N/A

**Gigabyte Technology**

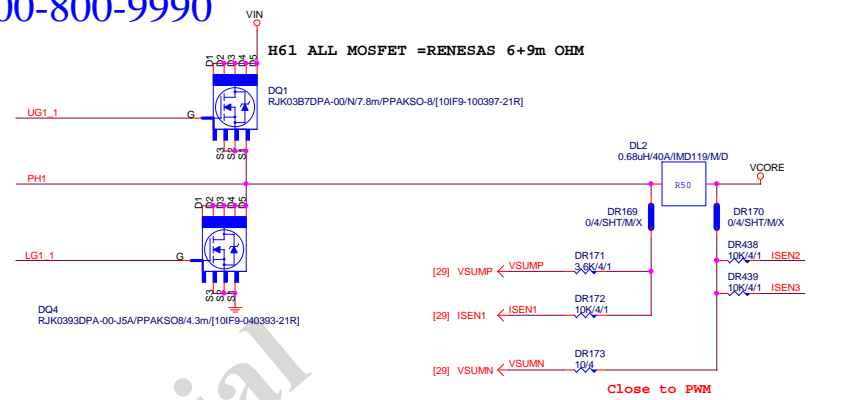
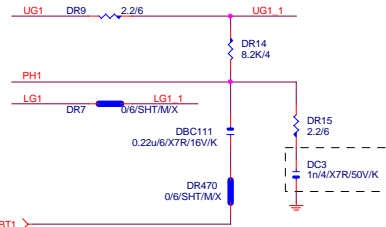
Title			
CPU CORE VR-2			
Size	Document Number	Rev	
Custom	GA-H61M-S2PT	1.0	
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## PHASE 1

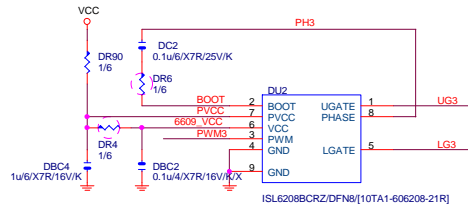


[1]

[29] BT1

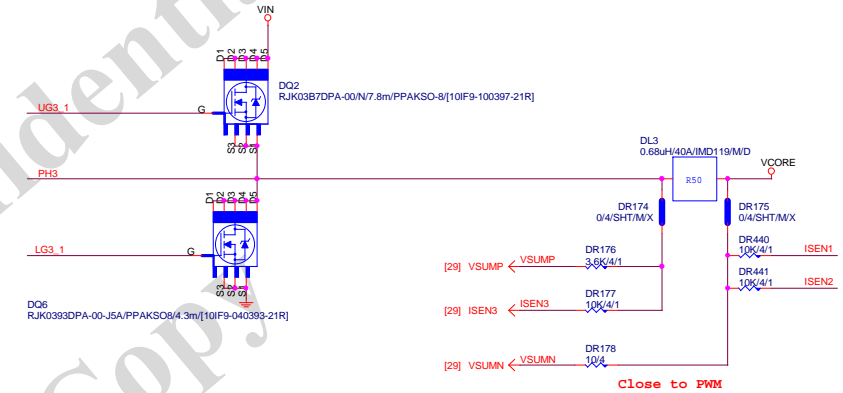
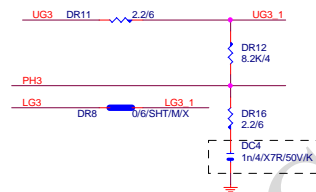
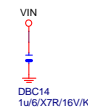


## PHASE 3

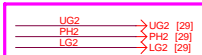
Pop ISL6625CB for PS1  
[ISL6625CBZ/S08]

PWM3 → PWM3 [29]

[3]

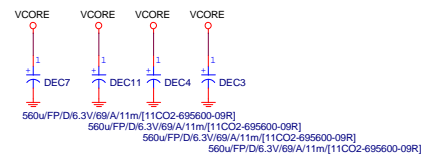
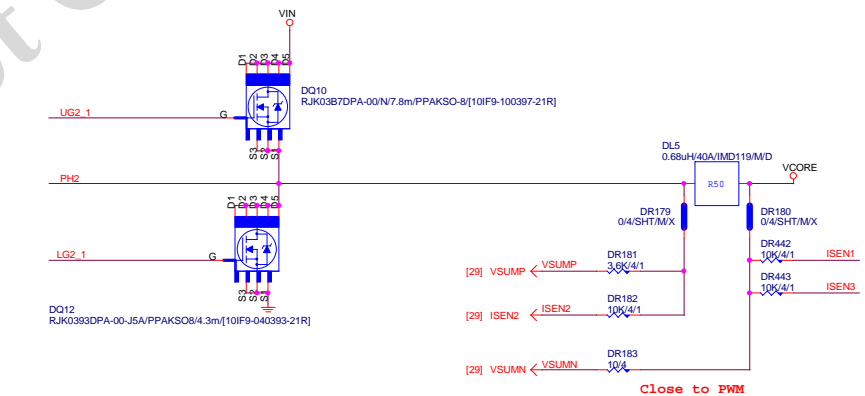
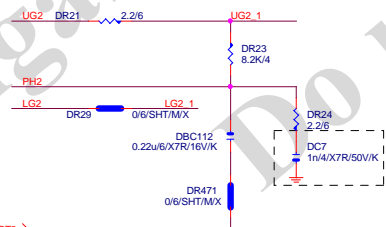


## PHASE 2



[2]

[29] BT2



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CPU CORE VR-3

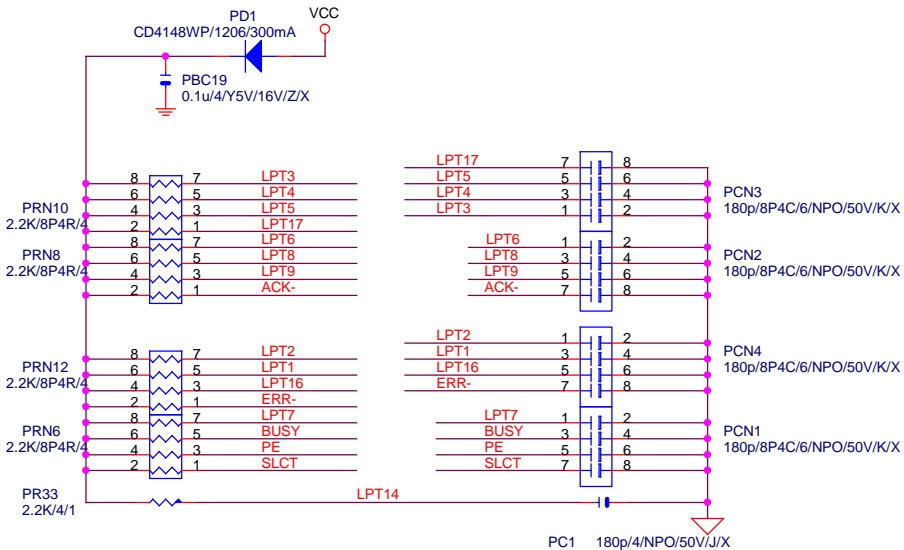
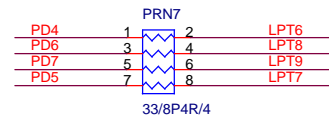
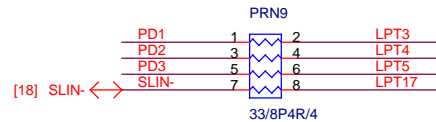
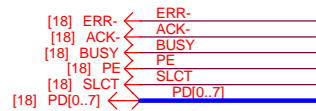
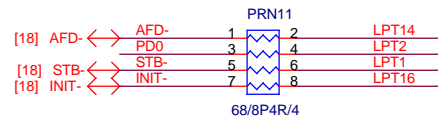
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TPM

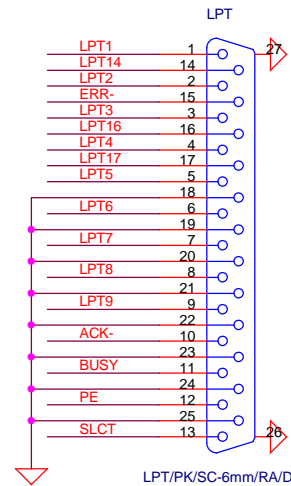
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LPT PORT

【技術通報R&D技術通報151】  
33ohm Change to 68ohm

COMB

N/A



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Custom			Rev 1.0	
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